

ADAPTIVE COMPUTING ENGINE (ACE)

FIG. 1

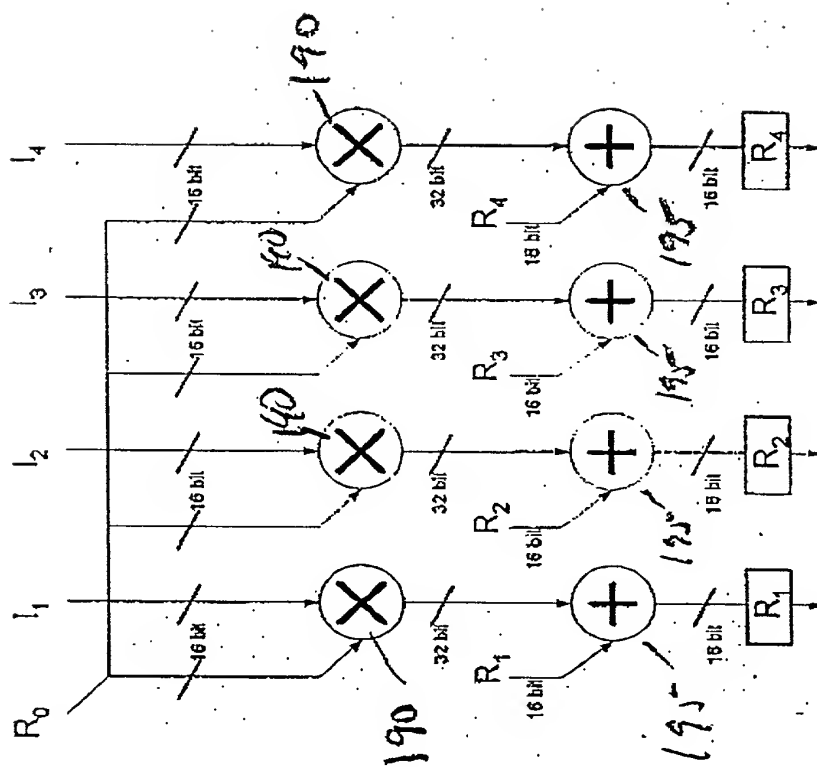


Fig. 2

TO OTHER MATRICES 150

(INCLUDING CONTROLLER 120 AND MEMORY 140)

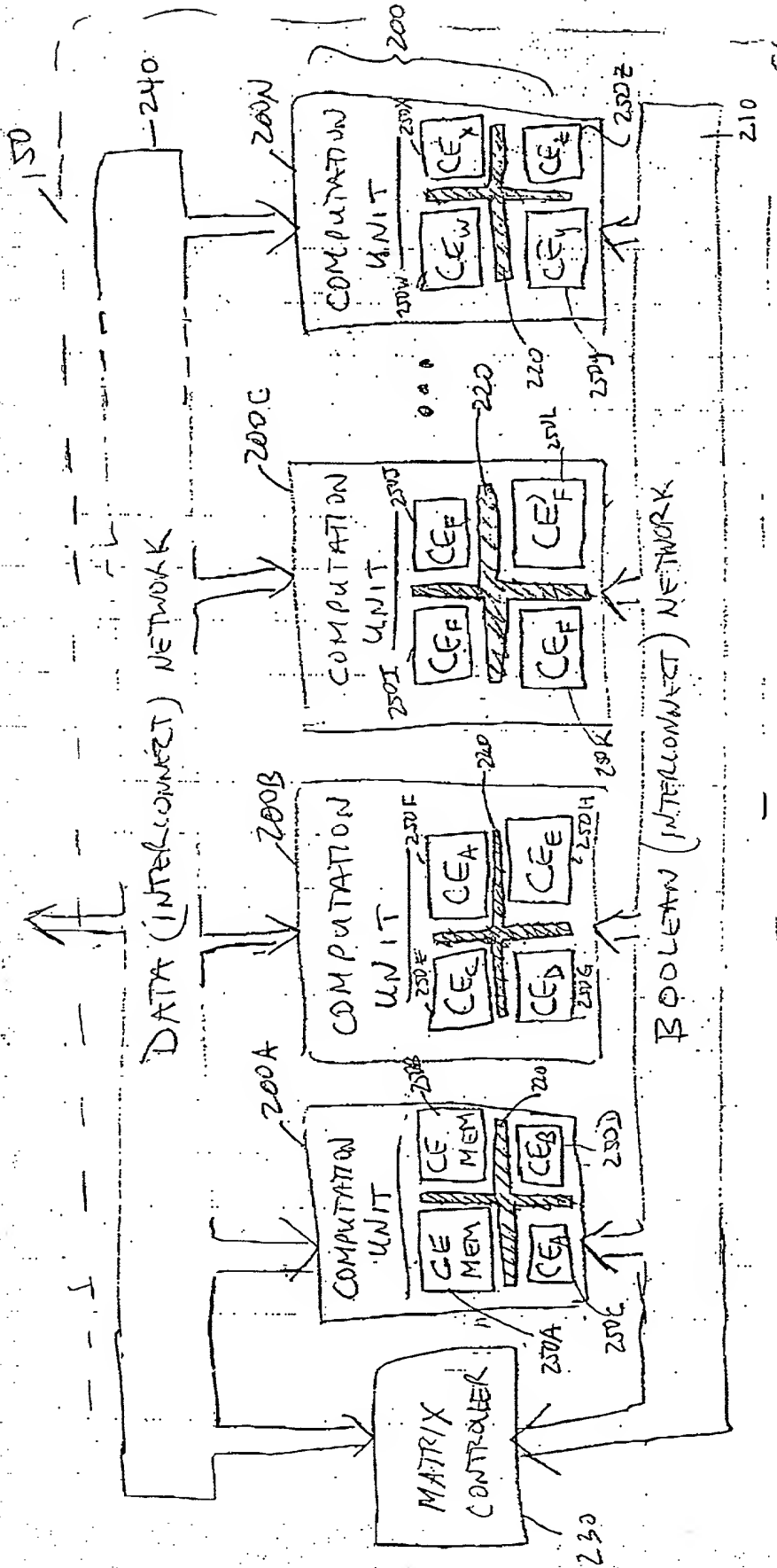


FIG. 3

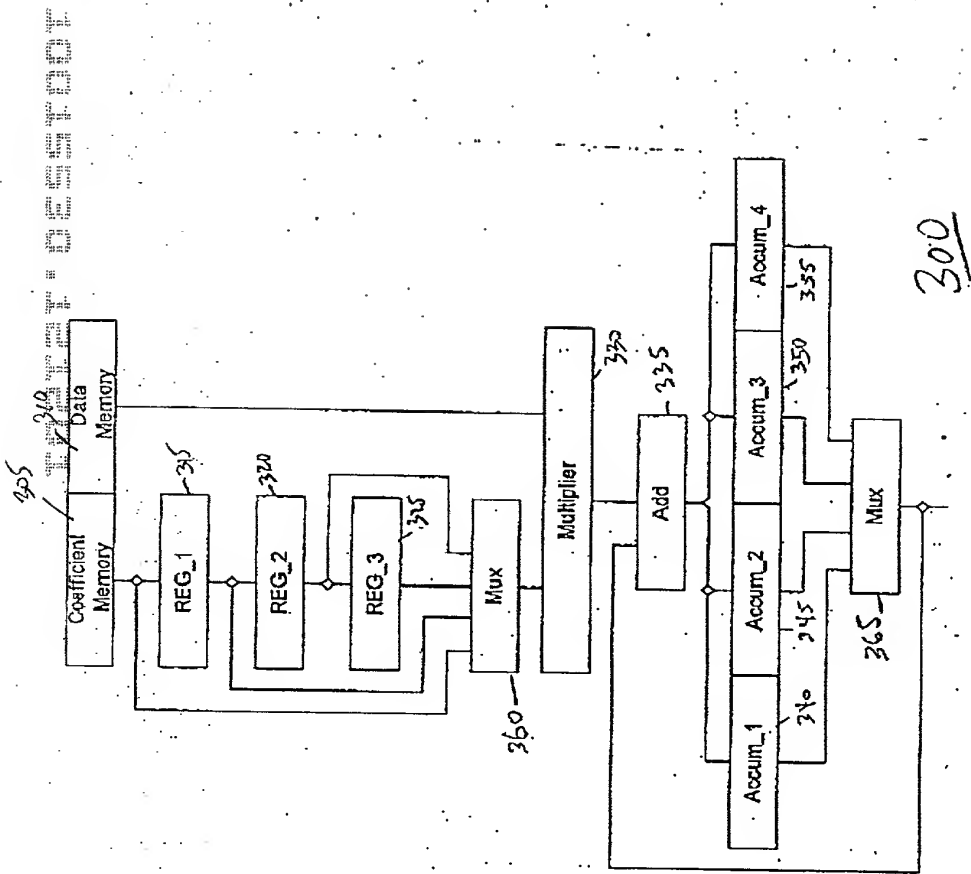
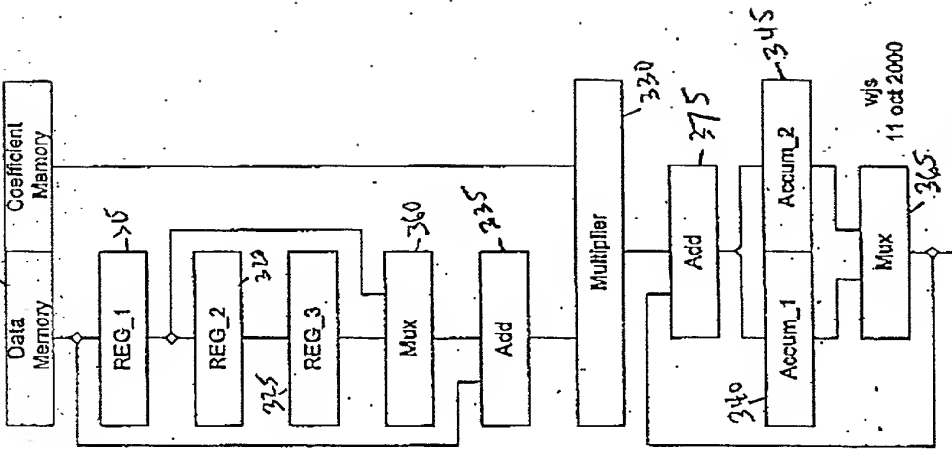


FIG. 5A

FIG. 5B

310, 305



370

FIG. 5B

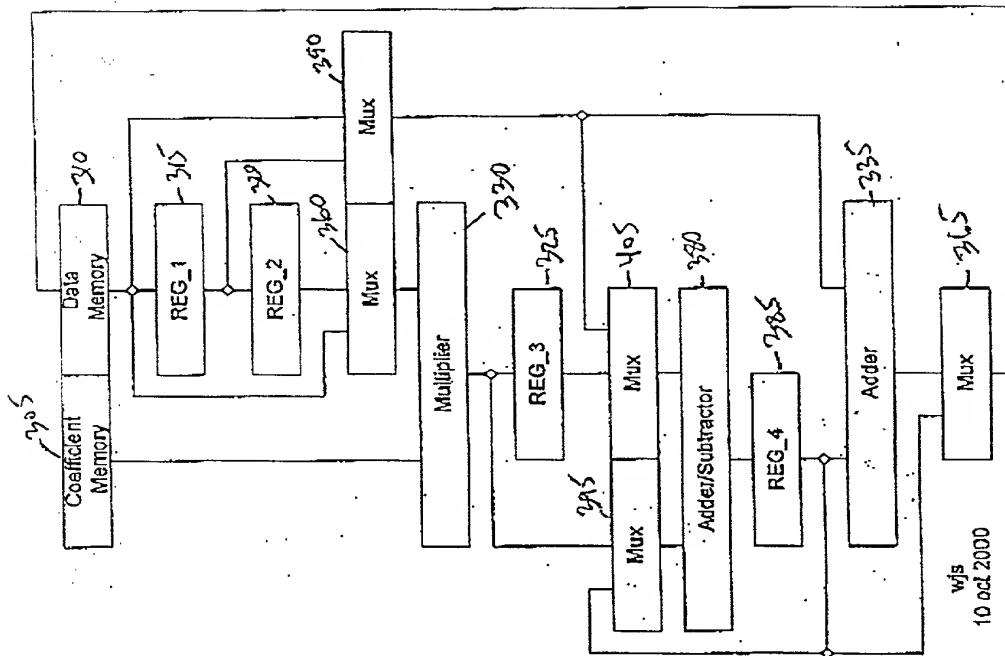


FIG. 5C

100

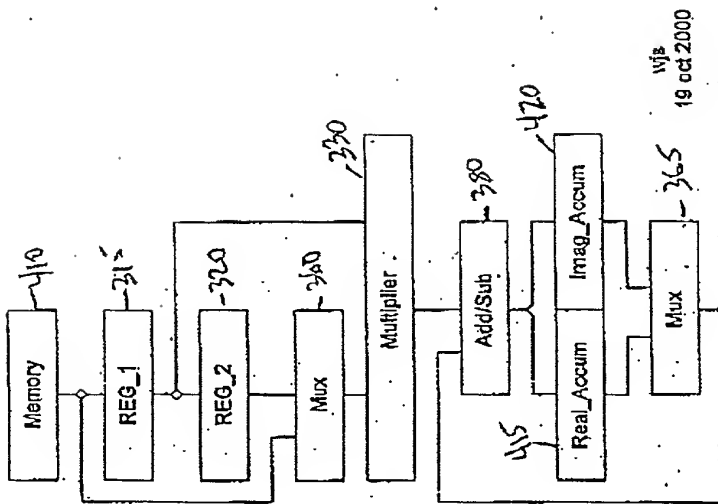
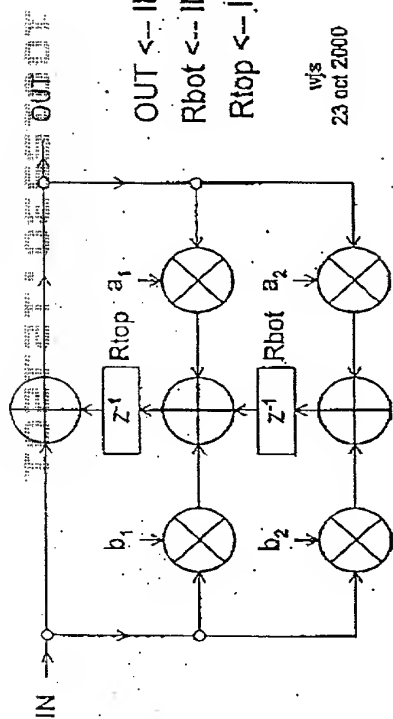


FIG. 5D

440

19 oct 2000



$$OUT \leftarrow IN + R_{top}$$

$$R_{bot} \leftarrow IN * b_2 + OUT * a_2$$

$$R_{top} \leftarrow IN * b_1 + OUT * a_1 + R_{bot}$$

W/S
23 oct 2000

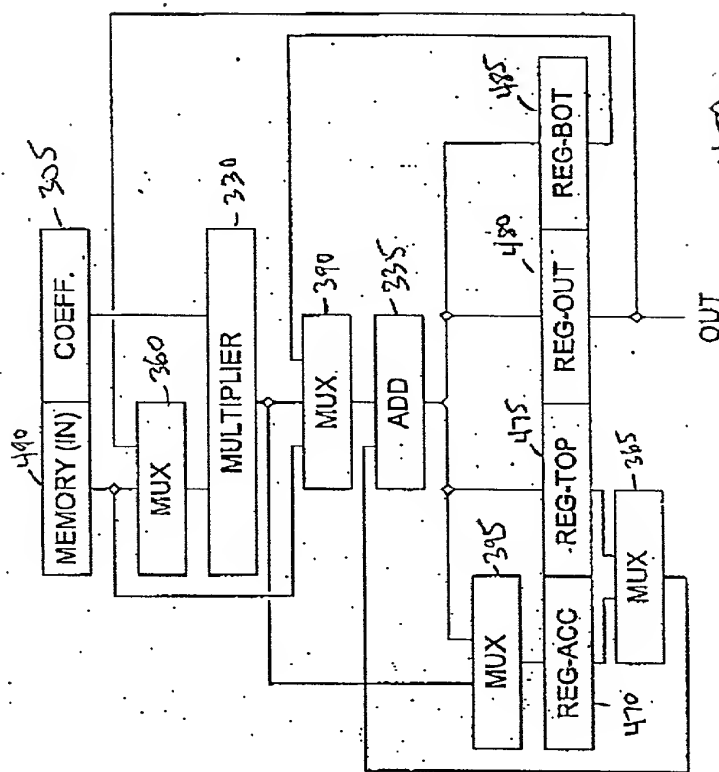


FIG. 5E

QUAD IN
PEER 3
PEER 2
PEER 1

505

REAL TIME INPUT

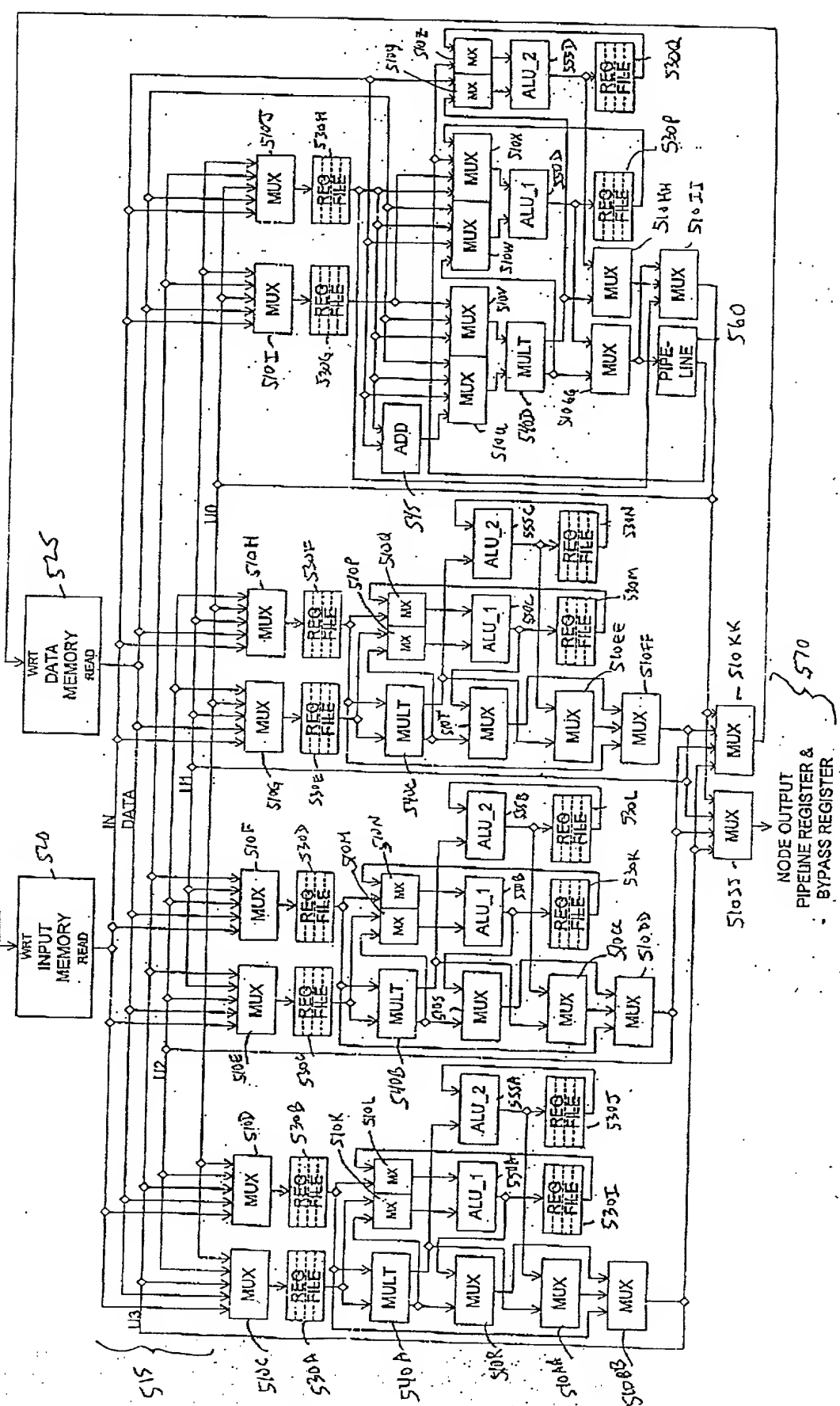
50A

50B

505

506

500



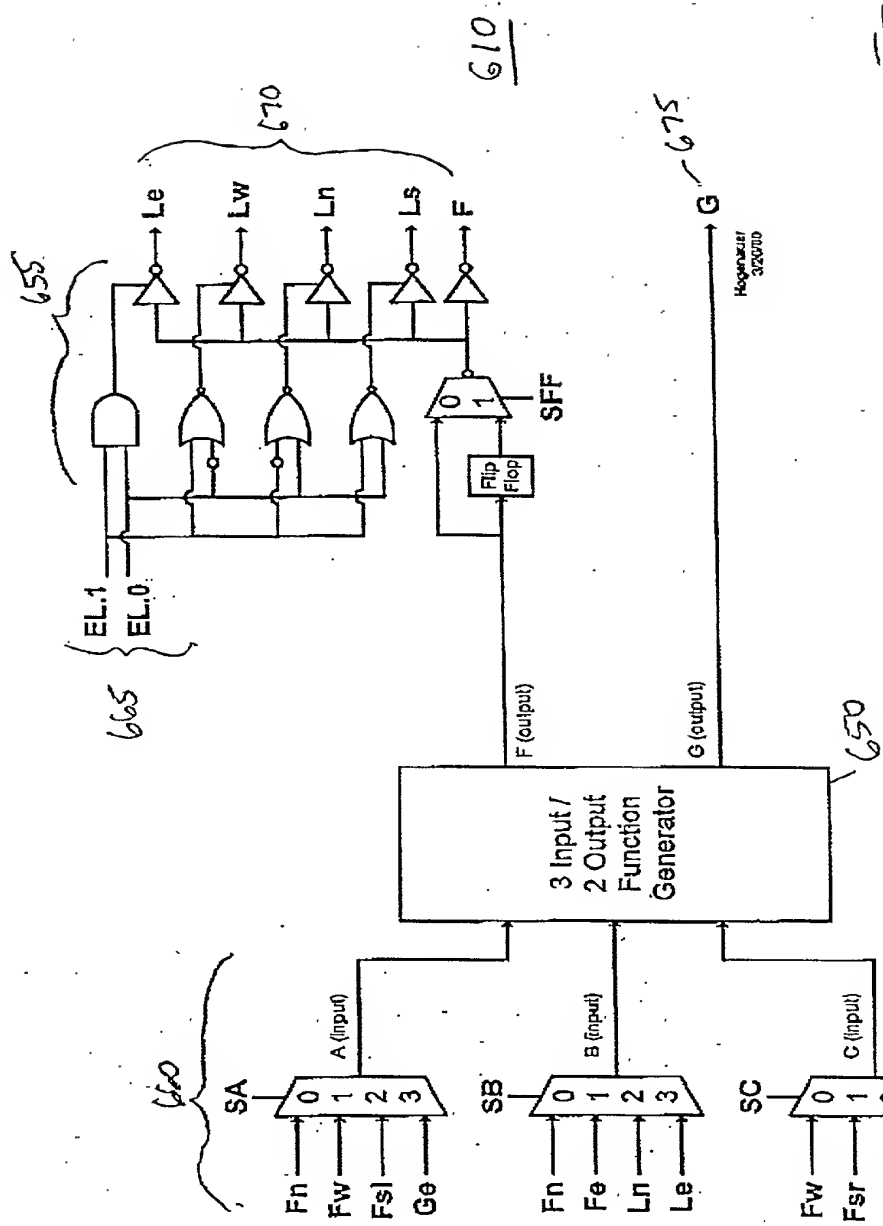


Fig. 8

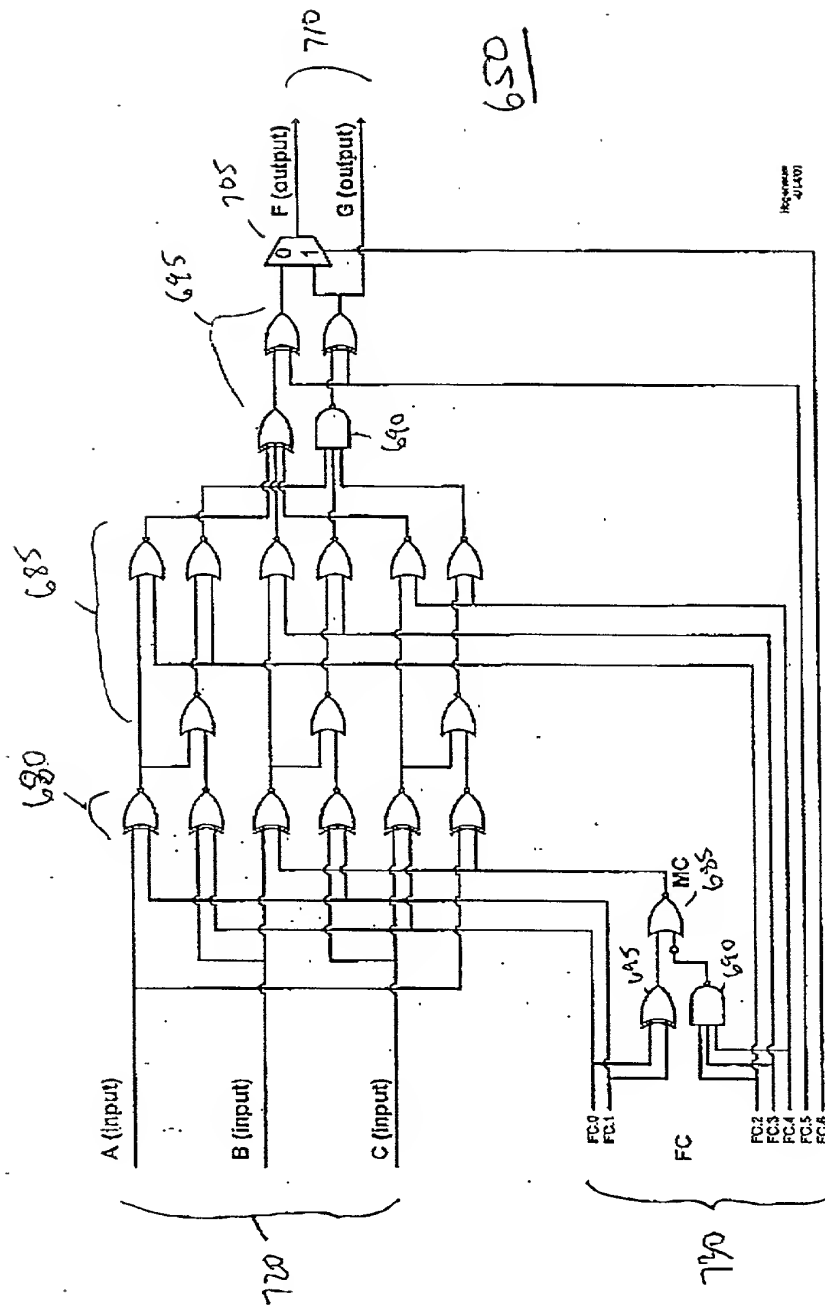
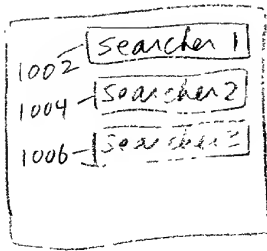
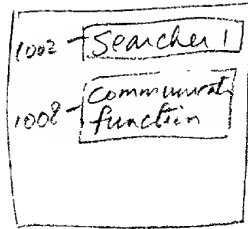


Fig. 9

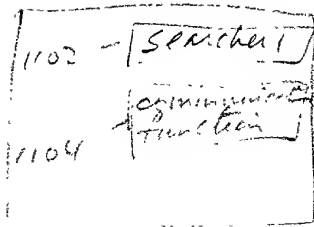


At power-up

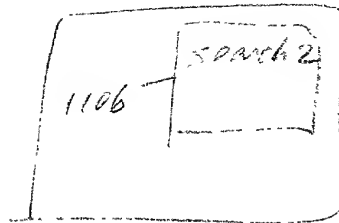


After system acquisition

FIG. 10

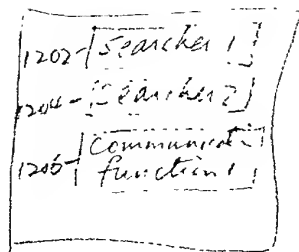


Before re-allocation

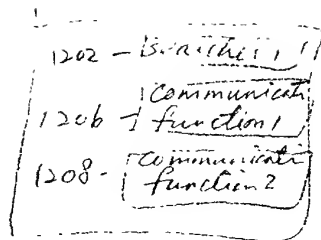


After re-allocation

FIG. 11

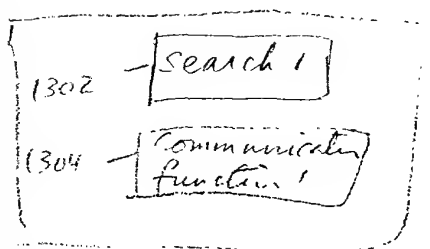


Before re-allocation

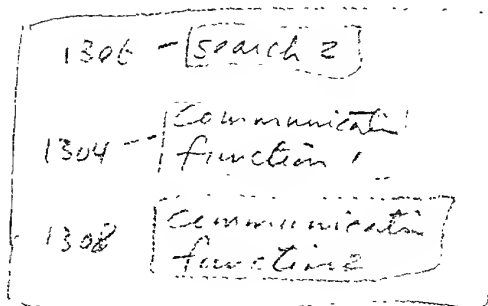


After re-allocation

FIG. 12



Before re-allocation



After re-allocation

FIG. 13